ENG 241: Digital Design School of Engineering University of Guelph 05-241 Fall 2020

## TA Responsibilities

## **Expectations:**

- 1. Be familiar with the Xilinx Vivado & Foundation Tools.
- 2. Be familiar with the Digilent FPGA Boards (NEXYS-3, NEXYS A7).
- 3. Be familiar with Chapters 1 through 8 of Mano 3rd Edition.
- 4. **Perform** the four Xilinx Foundation Tutorials found on my web page in the lab section (general info).
- 5. Supervise the labs on designated days.
- 6. Perform the labs assigned to the students at least one week before the students start their labs.
- 7. Read the lab assignments and inform if of any inconsistencies or mistakes.
- 8. Check all the groups for their preparation before starting the lab and make note of it (this will help later on in assigning a grade to the students).
- 9. All labs should be marked out of twenty five (25).
- 10. Read the homework assignments and point out any mistakes before handing them to the students.
- 11. Prepare Solutions for the Assignments.
- 12. Check the web page of the course and give me feedback about problems to links and missing information.
- 13. Allocate an hour a week for students as office hours starting from week #2.
- 14. Prepare spreadsheet for students grades.
- 15. Assist in marking the midterm exam and final exam.
- 16. Assist in marking the quizz (2 quizzes).
- 17. When a student sends you an email regarding a question and you respond to him/her then please copy me in the email.

05-241, Fall 2020 TA Responsibilities

Hours Allocated for Each Week					
Week	Date	Topic	Responsibility	A.S	A.E
#1	Sep 14	Lab#0	Familiarize students with Equipment	3 hr	3 hr
	Sep $15$		and WEB page Contents		
#2	Sep 21	Lab#1	Assist students with FPGA Board	7 hr	7 hr
	Sep 22		and accomplishing a simple design		
#3	Sep 28	Lab#2	Assist students understand Xilinx	7 hr	7 hr
	Sep 29		Foundation Tools (Lab#2)		
			Mark Lab1 and hand back to students	5 hr	5 hr
	Sep 30	Quiz#1	Mark Quiz #1	3 hr	3 hr
#4	Oct 05	Lab#3	Help students complete Lab#3	5 hr	5  hr
	Oct 06				
			Mark Lab2 and hand back to students	5 hr	5 hr
	Oct 07	Quiz#2	Mark Quiz #2	4 hr	4 hr
#5	Oct 12	VHDL	VHDL Tutorial (Section 0102)	4 hr	4 hr
	Oct 13	Tutorial	VHDL Tutorial (Section 0103)	4  hr	4  hr
			Mark Lab3 and hand back to students	5 hr	5 hr
#6	Oct 19	Lab#4	Assist students in Lab#4	6 hr	6 hr
	Oct 20				
#6	Oct 21	Midterm	Assist in Proctoring Exam	2 hr	2 hr
	Oct 22		Assist me in marking the exam	5 hr	5 hr
	Oct 24		Mark Lab4 and hand back to students	5 hr	5 hr
#7	Oct 26	Lab#5	Assist students in Lab#5	6 hr	6 hr
	Oct 27				
#8	Nov 2	Lab#6	Assist students in Lab#6	6 hr	6 hr
	Nov 3				
			Mark Lab5 and handback to students	5 hr	5 hr
#10	Nov 16	Lab#7	Assist students in Lab#7	5 hr	5 hr
	Nov 17				
	Nov 18		Mark Lab6 and hand back to students	6 hr	6 hr
#11	Nov 25	Quiz#2	Mark Quiz #2	4 hr	4 hr
	Nov 25		Mark Lab7 and hand back to students	6 hr	6 hr
#12	Nov 28	_	_	_	_
#13	Dec 05	FinalExam	Help Proctor the Final Exam.	3 hr	3 hr
#13	Dec 05		Assist me in marking the exam	10 hr	10 hr
#1-#12		Office Hours	Assist Students with Questions	11 hr	11 hr
#1-#12		Lab Prep	Preparation for Misc Labs	3 hr	3 hr
#1-#12		Misc	Lectures; marking; review; e.t.c	8 hr	8 hr
#1-#14			TOTAL # HOURS	140/140hr	140/140 hr
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